

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of the Claims:

1. (Currently Amended) A graphics device, comprising:

a bus interface unit including a plurality of bus signal buffers to couple the graphics device to a graphics bus, the bus signal buffers to receive a first set of signals to be used by the graphics device; and

a load balancing bus signal buffer to further couple the graphics device to the graphics bus, the load balancing bus signal buffer to receive a second set of signals not to be used by the graphics device, the load balancing bus signal buffer to provide load balancing on the graphics bus when a second graphics device is installed, the load balancing bus signal buffer is not connected to any internal circuits of the graphics device.

2. (Previously Presented) The graphics device of claim 1, wherein the graphics bus is an accelerated graphics port (AGP) bus.

3. (Previously Presented) The graphics device of claim 2, wherein the graphics device is an AGP 2X device.

4. (Previously Presented) The graphics device of claim 3, wherein the upgrade graphics device is an AGP 4X device.

5. (Previously Presented) The graphics device of claim 4, the load balancing bus signal buffer to provide load balancing for a first address/data bus strobe compliment signal on the AGP bus.

6. (Previously Presented) The graphics device of claim 4, the load balancing bus signal buffer to provide load balancing for a second address/data bus strobe compliment signal on the AGP bus.

7. (Previously Presented) The graphics device of claim 4, the load balancing bus signal buffer to provide load balancing for a sideband strobe compliment signal on the AGP bus.

8. (Currently Amended) An apparatus, comprising:

a graphics bus;

a graphics device coupled to the graphics bus, the graphics device including

a bus interface unit including a plurality of bus signal buffers to couple the graphics device to the graphics bus, the bus signal buffers to receive a first set of signals to be used by the graphics device;~~and~~

a load balancing bus signal buffer to further couple the graphics device to the graphics bus, the load balancing bus signal buffer to receive a second set of signals not to be used by the graphics device, the load balancing bus signal buffer to provide load balancing on the graphics bus when a second graphics device is installed, the load balancing bus signal buffer is not connected to any internal circuits of the graphics device; and

a second graphics device connector to receive a second graphics device, the second graphics device connector to couple the second graphics device to the graphics bus.

9. (Previously Presented) The apparatus of claim 8, wherein the graphics bus is an accelerated graphics port (AGP) bus.

10. (Previously Presented) The apparatus of claim 9, wherein the graphics device is an AGP 2X device.

11. (Previously Presented) The apparatus of claim 10, the upgrade graphics device connector to receive an AGP 4X device.

12. (Previously Presented) The apparatus of claim 11, the load balancing bus signal buffer to provide load balancing for a first address/data bus strobe compliment signal on the AGP bus.

13. (Previously Presented) The apparatus of claim 11, the load balancing bus signal buffer to provide load balancing for a second address/data bus strobe compliment signal on the AGP bus.

14. (Previously Presented) The apparatus of claim 11, the load balancing bus signal buffer to provide load balancing for a sideband strobe compliment signal on the AGP bus.

15. (Currently Amended) A method, comprising:

coupling a graphics device to a graphics bus, the graphics device including a plurality of bus signal buffers to couple the graphics device to the graphics bus, the bus signal buffers to receive a first set of signals to be used by the graphics device; and

providing a load balancing bus signal buffer to further couple the graphics device to the graphics bus, the load balancing bus signal buffer to receive a second set of signals not to be used by the graphics device, the load balancing bus signal buffer to provide load balancing on the graphics bus when an upgrade graphics device is installed, the load balancing bus signal buffer is not connected to any internal circuits of the graphics device.

16. (Previously Presented) The method of claim 15, wherein providing a load balancing bus signal buffer includes the step of providing a load balancing bus signal buffer for an address/data bus strobe compliment signal on an accelerated graphics port (AGP) bus.

17. (Previously Presented) The method of claim 15, wherein providing a load balancing bus signal buffer includes the step of providing a load balancing bus signal buffer for a sideband strobe compliment signal on an AGP bus.